

Figure 1

FIG. 2 is a block diagram of a computer system 200. The system 200 includes a processor 202, a bus 201, main memory 204, read only memory 206, and a mass storage device 207. The processor 202 is connected to the bus 201. The main memory 204, read only memory 206, and mass storage device 207 are also connected to the bus 201. The system 200 is connected to a display 221, a keyboard 222, a cursor control device 223, and a communication device 225.

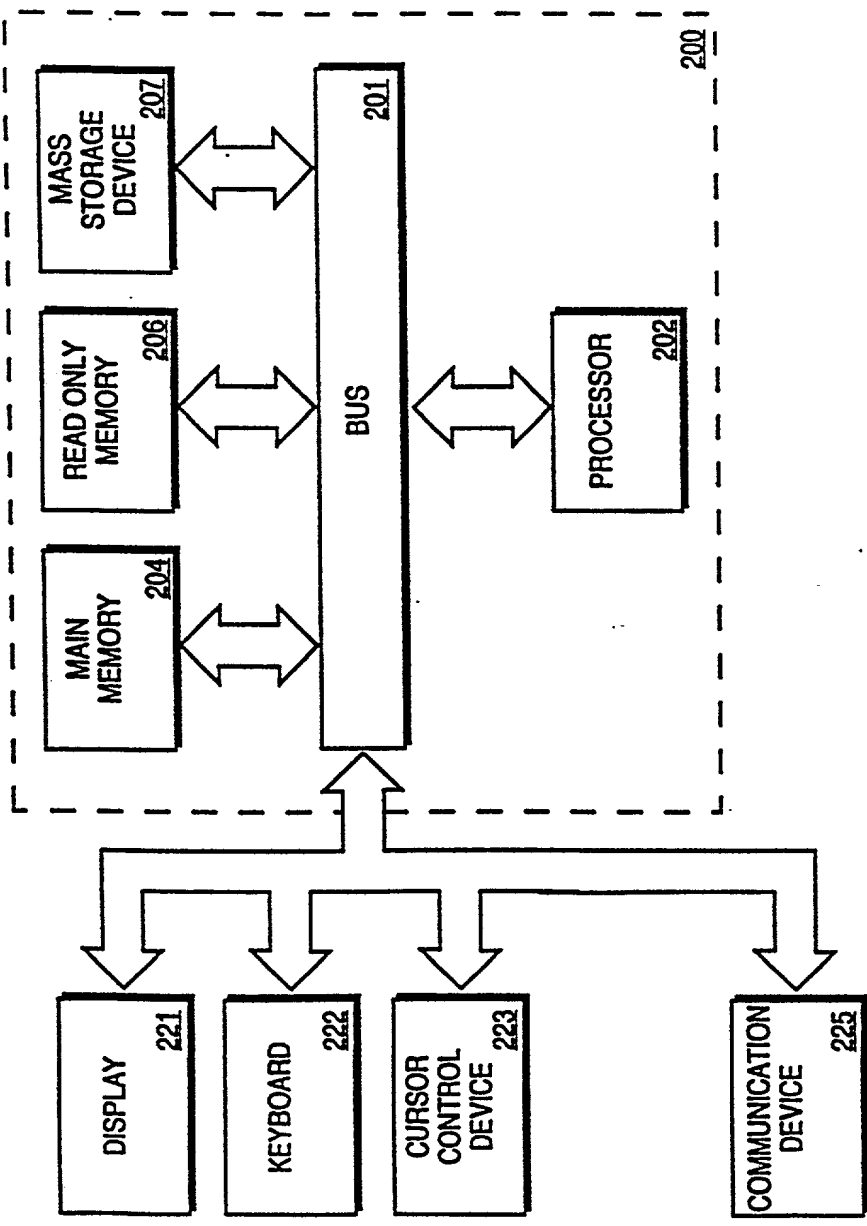


FIG. 2

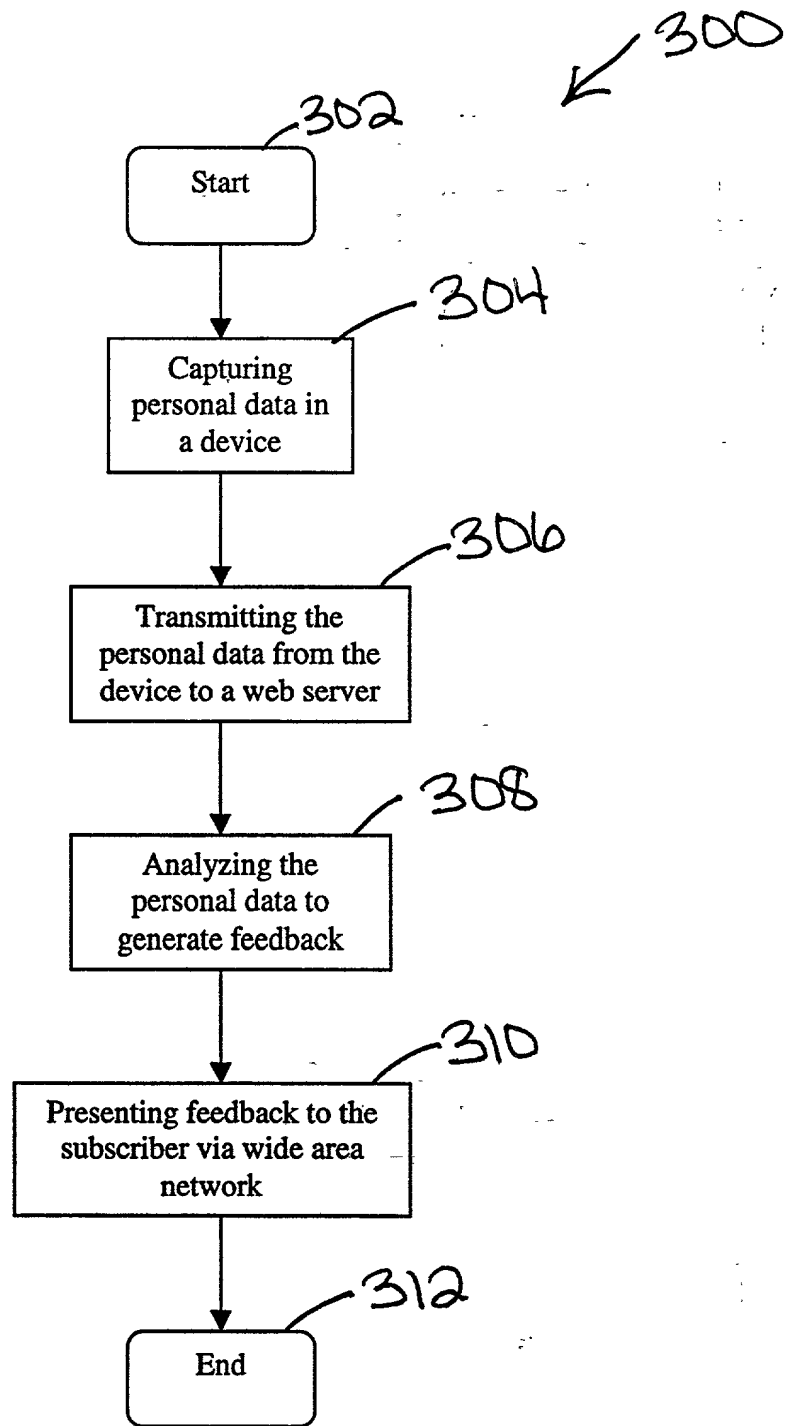


Figure 3

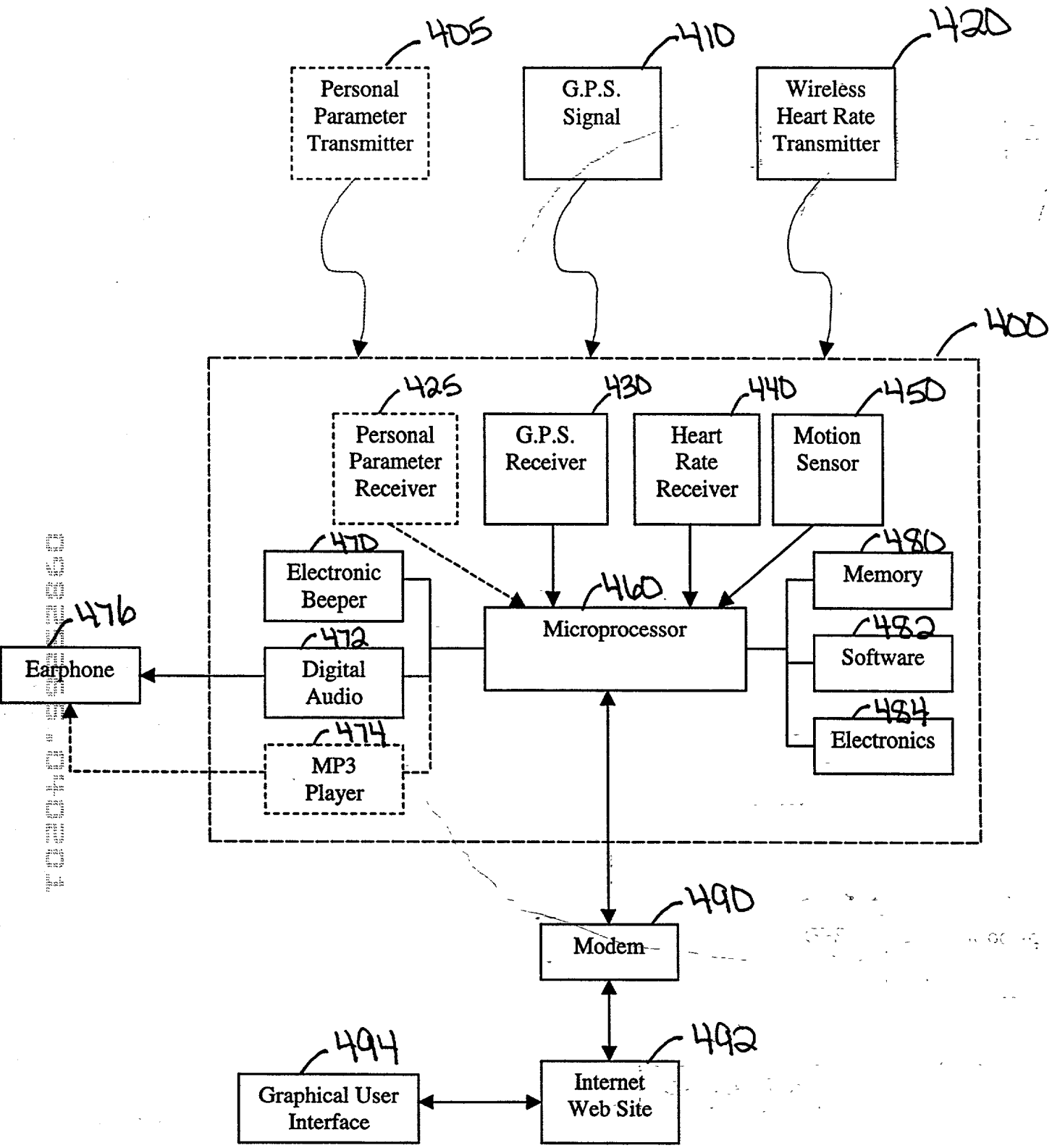


Figure 4

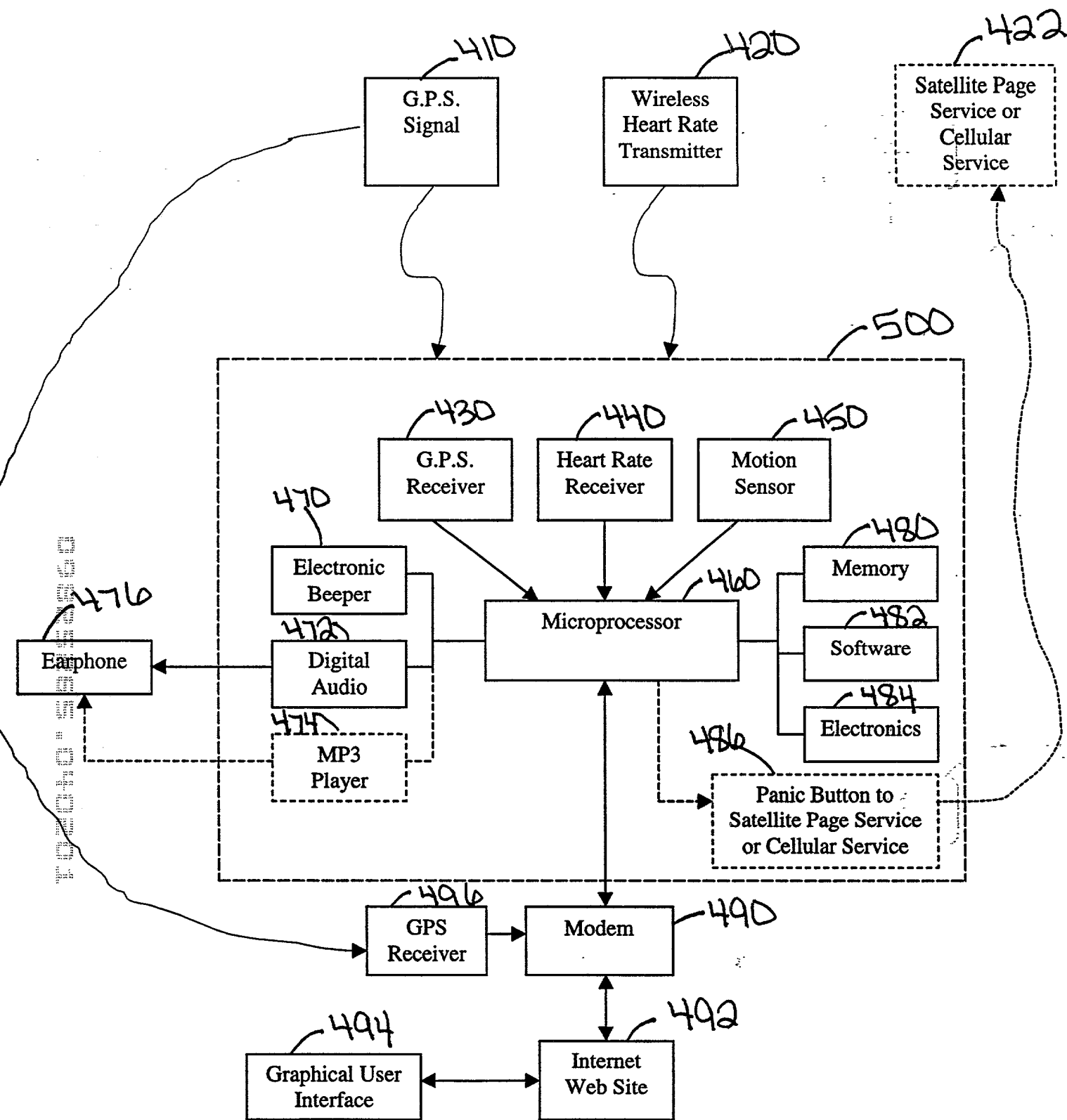


Figure 5

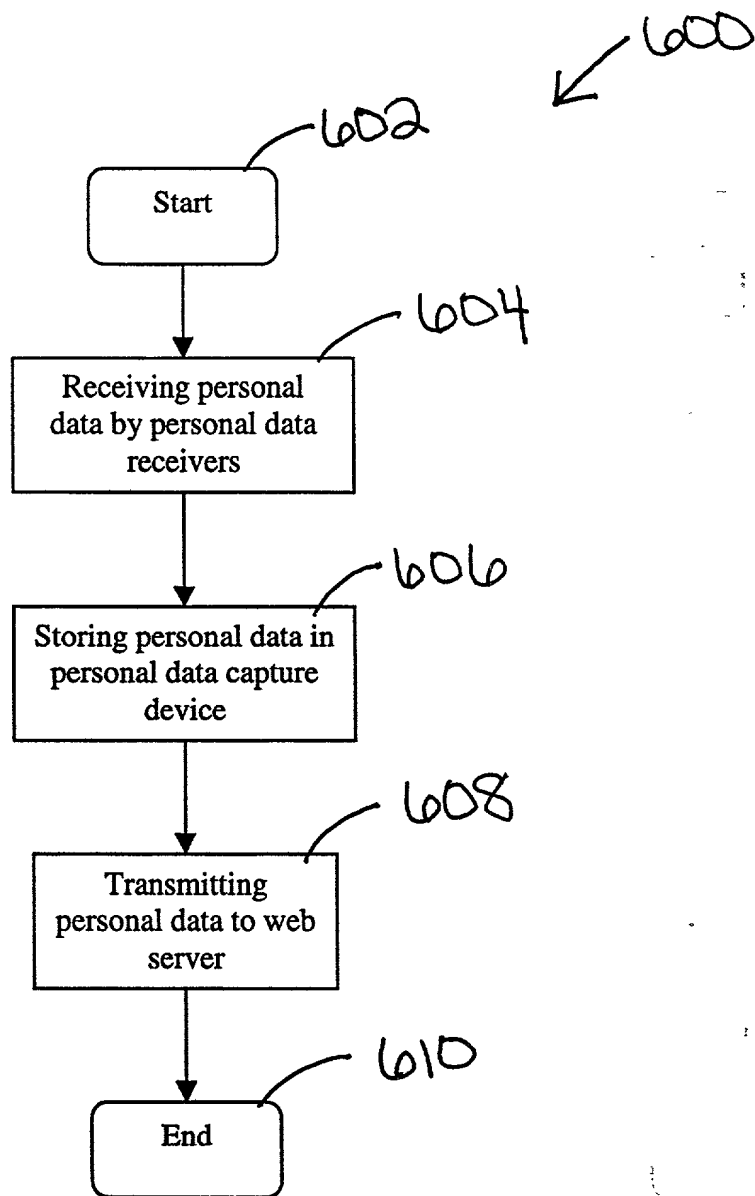


Figure 6

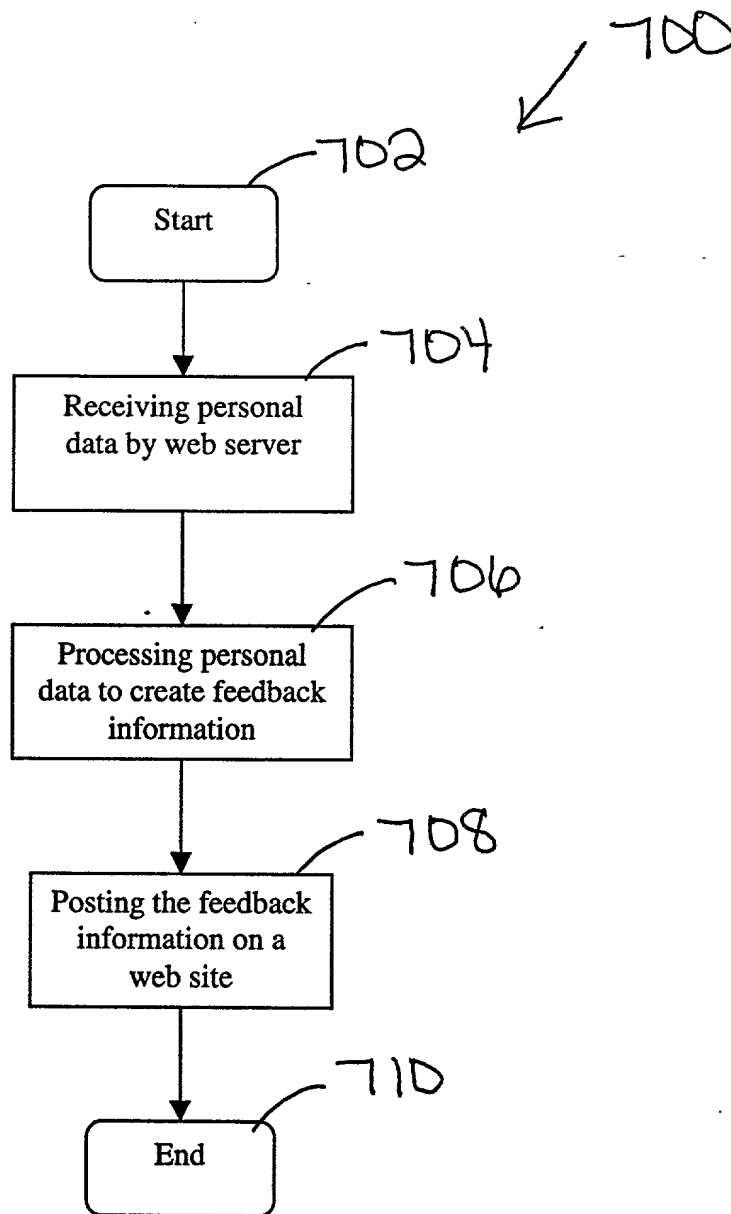


Figure 7

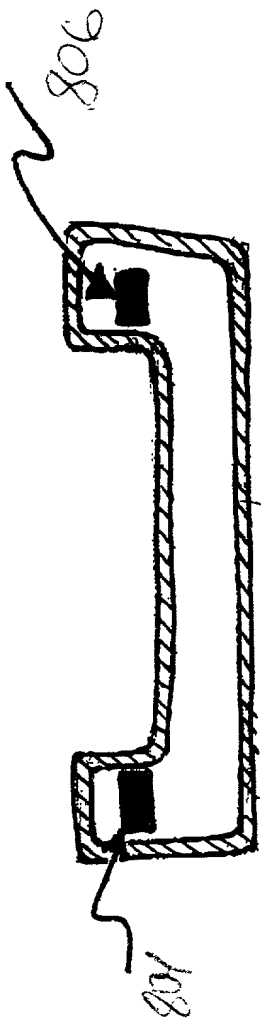
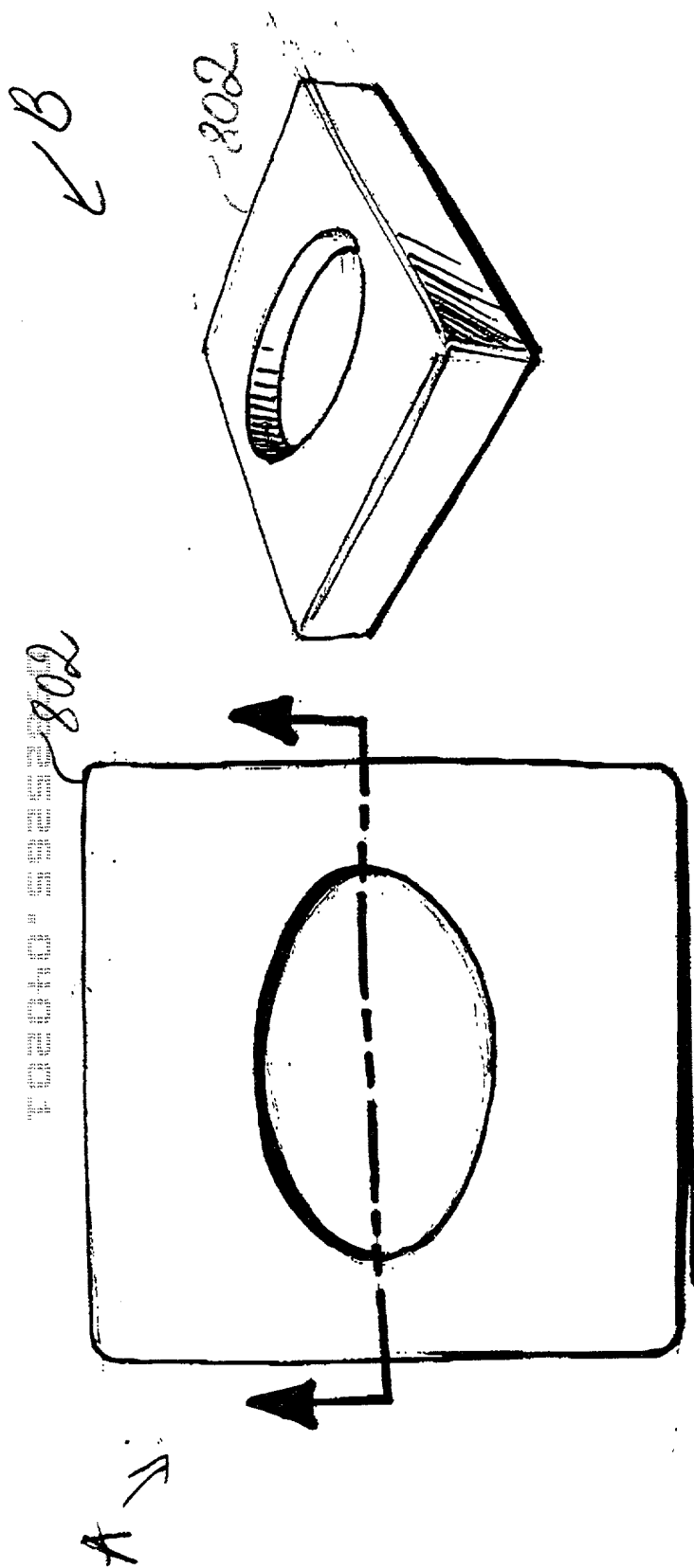
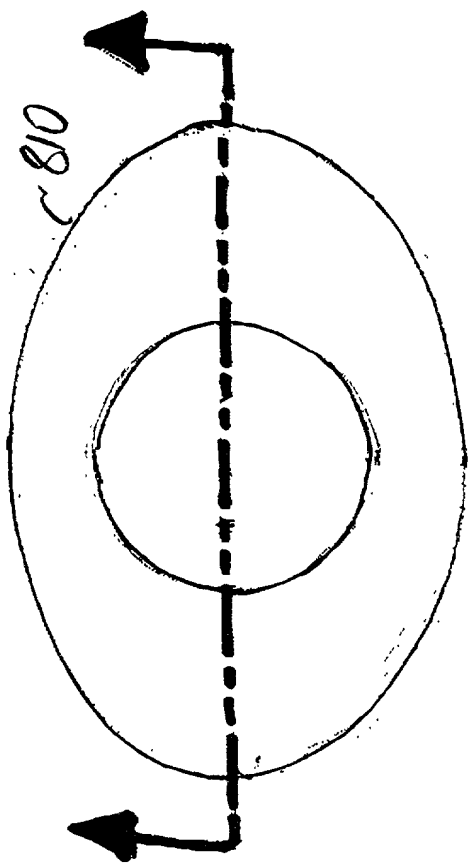


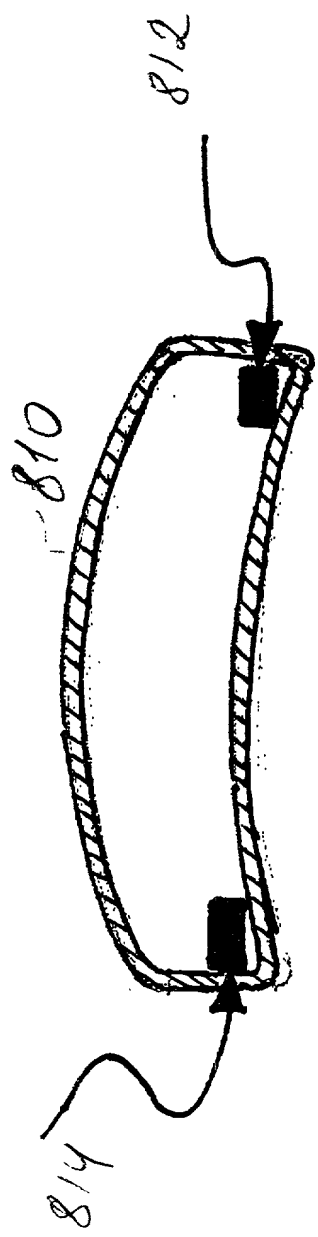
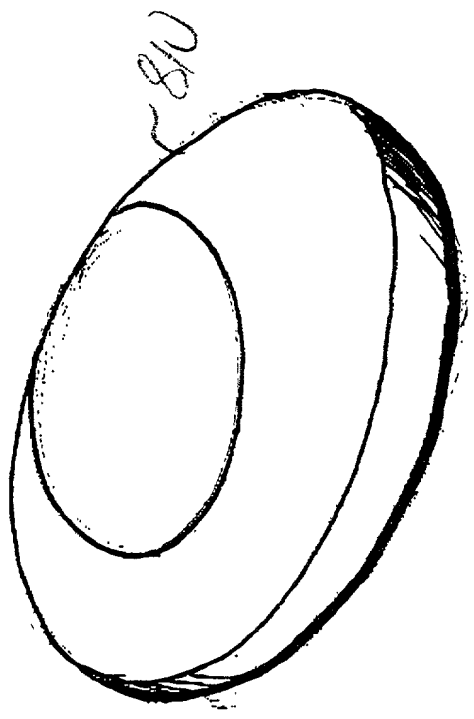
Fig 8A

FIG. 88 is a schematic diagram of a device 800 in a first state. The device 800 includes a first member 810 and a second member 812. The first member 810 is a ring-shaped member and the second member 812 is a rectangular member. The first member 810 is positioned around the second member 812. The device 800 is shown in a perspective view.

A



B



C

Fig. 88

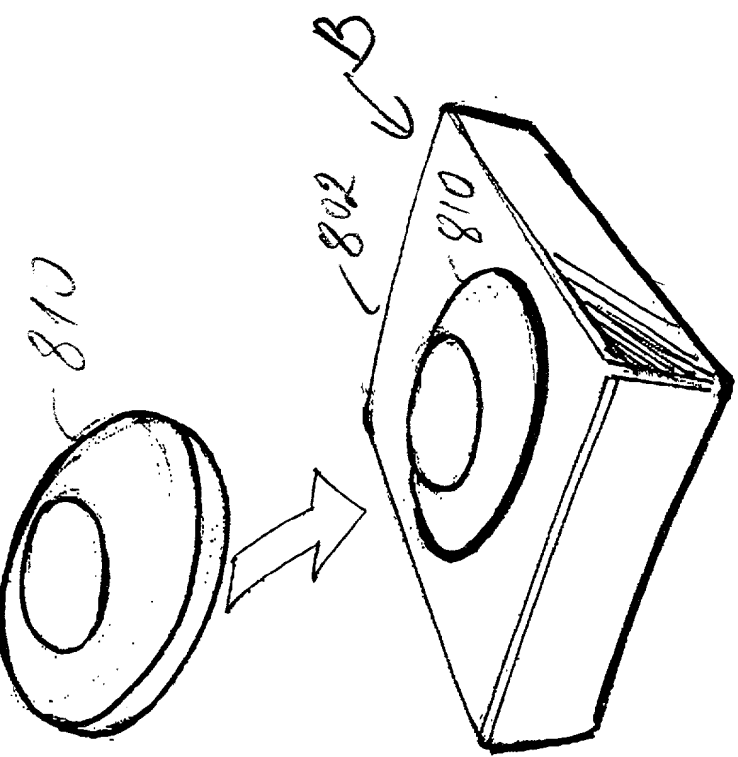


Fig. 8C

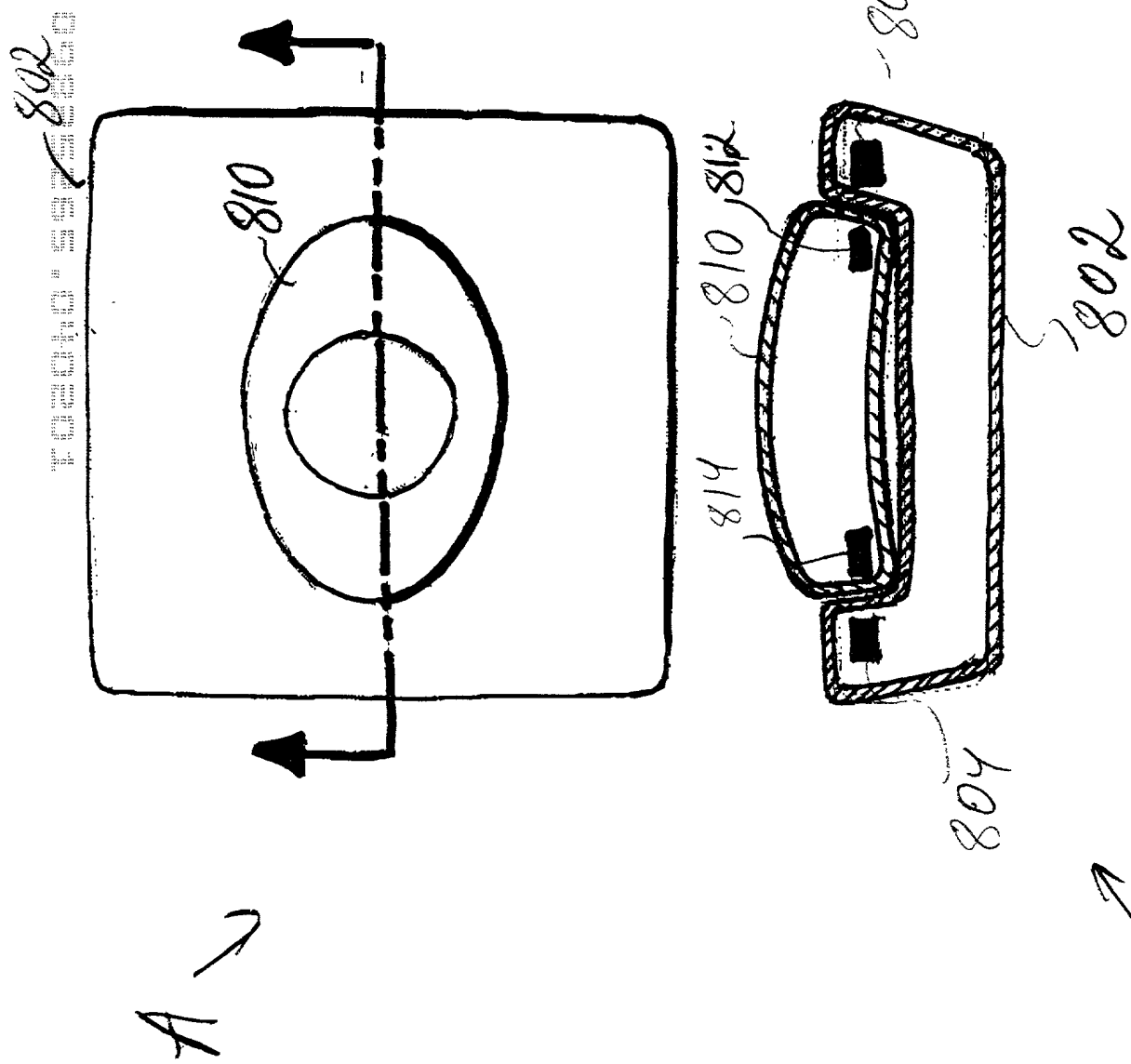


Fig. 8B

900

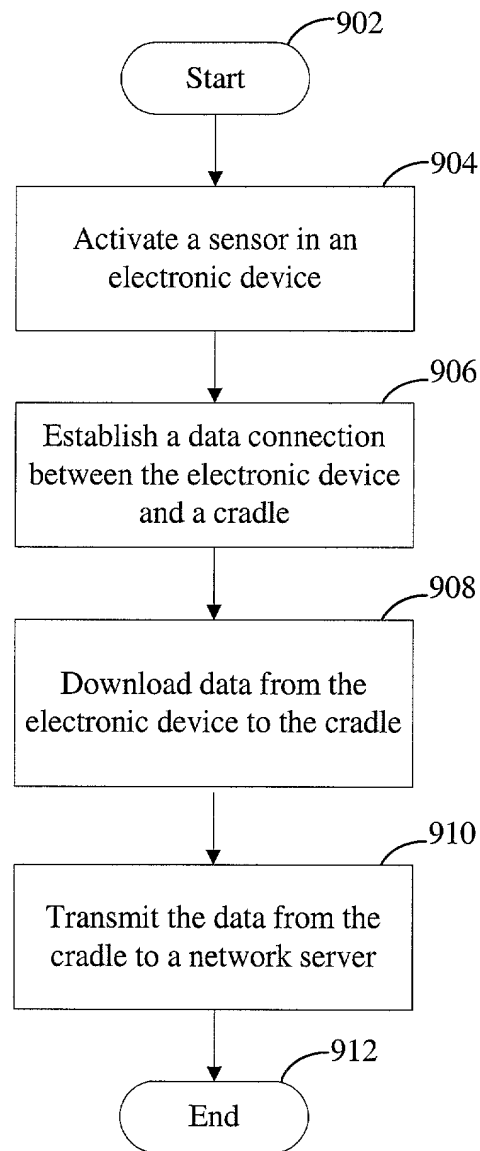


FIG. 9